

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT: Christopher J. Diorio et al.
SERIAL NO.: 10/681,577
FILING DATE: 10/02/03
TITLE: Use of Analog-Valued Floating-Gate Transistors to Match the
Electrical Characteristics of Interleaved and Pipelined Circuits
EXAMINER: Nguyen, Hai L.
ART UNIT: 2816

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450, on the date printed below:

Name:

Ruth O. Rodriguez

Date:

3/16/05

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action mailed December 16, 2004, please amend the subject application as indicated.

Amendments to the Claims are reflected in the Listing of Claims beginning on page 2.

Amendments to the Drawings are reflected in page 15 and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks begin on page 16.